

## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

PLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION N
09/845,562	04/30/2001	Lee D. Whetsel	TI-31206	2216
23494	7590 07/12/2005	EXAMINER CHUNG, PHUNG M		
	TRUMENTS INCORPOR			
P O BOX 655474, M/S 3999 DALLAS, TX 75265			ART UNIT	PAPER NUMBER
			7867 61417	TAI ER NOMBER

Please find below and/or attached an Office communication concerning this application or proceeding.

7						
	Application No.	Applicant(s)				
Office Astion Occurrence	09/845,562	WHETSEL, LEE D.				
Office Action Summary	Examiner	Art Unit				
	Phung My Chung	2133				
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence address				
A SHORTENED STATUTORY PERIOD FOR RETHE MAILING DATE OF THIS COMMUNICATION Extensions of time may be available under the provisions of 37 CFI after SIX (6) MONTHS from the mailing date of this communication If the period for reply specified above is less than thirty (30) days, a If NO period for reply is specified above, the maximum statutory pe Failure to reply within the set or extended period for reply will, by st Any reply received by the Office later than three months after the mearned patent term adjustment. See 37 CFR 1.704(b).	ON. R 1.136(a). In no event, however, may a r . I reply within the statutory minimum of thir riod will apply and will expire SIX (6) MON atute, cause the application to become AE	reply be timely filed ty (30) days will be considered timely. ITHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on E	RCE dated on 5/2/05.					
<u> </u>						
3) Since this application is in condition for allo	-					
closed in accordance with the practice und	er <i>Ex parte Quayl</i> e, 1935 C.D	). 11, 453 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>14-18</u> is/are pending in the application	ation.					
4a) Of the above claim(s) is/are with						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>14-18</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s)are subject to restriction ar	id/or election requirement.					
Application Papers	•					
9) The specification is objected to by the Exam	niner.					
10) The drawing(s) filed on is/are: a)						
Applicant may not request that any objection to	-	• •				
Replacement drawing sheet(s) including the cor						
	e Examiner, Note the attached	Office Action or form P10-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for fore	eign priority under 35 U.S.C. §	119(a)-(d) or (f).				
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority docum						
2.☐ Certified copies of the priority docum 3.☐ Copies of the certified copies of the priority docum		· ·				
application from the International But		received in this National Stage				
* See the attached detailed Office action for a		received.				
	·					
Attachment(s)						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview S	ummary (PTO-413) s)/Mail Date				
Notice of Dransperson's Patent Drawing Review (PTO-948)     Information Disclosure Statement(s) (PTO-1449 or PTO/SB/Paper No(s)/Mail Date		nformal Patent Application (PTO-152)				
U.S. Patent and Trademark Office						
PTOL-326 (Rev. 1-04) Office	e Action Summary	Part of Paper No./Mail Date 20050708				

Page 2

Application/Control Number: 09/845,562

Art Unit: 2133

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be

negatived by the manner in which the invention was made.

2. Claims 14-18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Whetsel

(5,606,566) in view of Muradali et al (6,586,981).

As per claim 14, Whetsel discloses a dual mode test port, comprising:

a scan data input terminal for inputting serial data;

a scan data output terminal for outputting serial data;

a scan clock input terminal for timing the operation of the test port; and

a control input terminal for controlling the operation of the test port according to one of a

first scan mode and second scan mode. (See col. 1, line 50 to line 24 of col. 2 and col. 13, lines

62-65). Whetsel does not disclose that the test port is on an embedded core within an integrated

circuit. However, Muradali et al disclose a test port that is on an embedded core within an

integrated circuit. (See Abstract, col. 3, lines 56-63, Fig. 3D and col. 5, lines 26-45). Therefore,

it would have been obvious to a person of ordinary skill in the art, at the time the invention was

made, to incorporate the test port of Muradali et al into the invention of Whetsel to permit scan

testing of scan elements in the cores by providing alternate scan paths which share access to

Application/Control Number: 09/845,562

Art Unit: 2133

limited data test ports of the integrated circuit under test and to increase the efficiency of scan testing.

As per claim 15, the teaching of Whetsel and Muradali et al have been discussed above. Whetsel further disclose in which the first scan mode is the IEEE 1149.1 protocol for performing one of a boundary scan test. (See col. 1, lines 50-64).

As per claim 16, the teaching of Whetsel and Muradali et al have been discussed above. Muradali et al further disclose the second scan mode is an internal scan protocol for performing an internal scan test operation within the embedded core circuit. (See col. 5, lines 26-35).

As per claims 17 and 18, the teaching of Whetsel and Muradali et al have been discussed above. Whetsel further discloses including circuitry for selecting the test port to respond to the control input terminal according to the first scan mode and, alternately, for selecting the test port to respond to the control input terminal according to the second scan mode. (See col. 13, lines 62-65).

- 3. Applicant's arguments with respect to claims 1-13 have been considered but are moot in view of the new ground(s) of rejection.
- 4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phung My Chung whose telephone number is 571-272-3818. The examiner can normally be reached on Monday-Friday.

Application/Control Number: 09/845,562 Page 4

Art Unit: 2133

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 571-272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Phung My Chung
Primary Ratent Examiner
Technology Center 2100